

## Electronic Acknowledgement Receipt

EFS ID:	1116961
Application Number:	10615139
Confirmation Number:	8097
Title of Invention:	Method for preparing thin integrated circuits with multiple circuit layers
First Named Inventor:	Jung-Chien Chang
Customer Number:	4586
Filer:	Cynthia L. Pillote/Julie Eslick
Filer Authorized By:	Cynthia L. Pillote
Attorney Docket Number:	MR929-893
Receipt Date:	18-JUL-2006
Filing Date:	09-JUL-2003
Time Stamp:	12:33:03
Application Type:	Utility
International Application Number:	

### Payment information:

Submitted with Payment	no
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### File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part	Pages
1		395240069ROA.pdf	361421	yes	11

	Multipart Description			
	Doc Desc	Start	End	
	Amendment - After Non-Final Rejection	1	1	
	Claims	2	6	
	Applicant Arguments/Remarks Made in an Amendment	7	11	

**Warnings:**

**Information:**

2	Power of Attorney (may include Associate POA)	395240069POA.pdf	39562	no	1
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**Warnings:**

**Information:**

Total Files Size (in bytes):	400983
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**New Applications Under 35 U.S.C. 111.**  
If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

**National Stage of an International Application under 35 U.S.C. 371**  
If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.